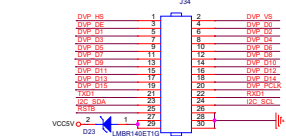
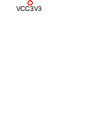
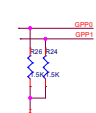
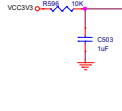
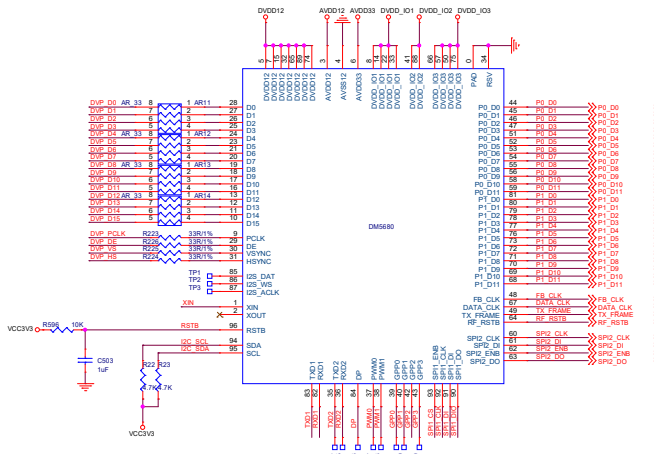
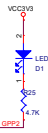
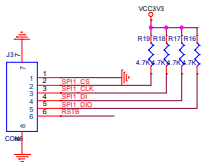
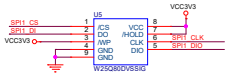
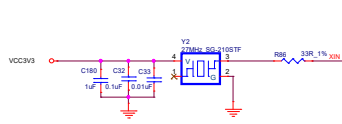
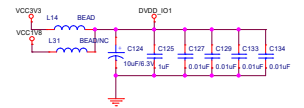


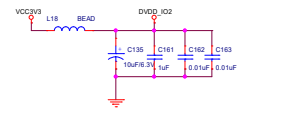
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Size: B	Document Number
Block Diagram	
Date: Saturday, April 20, 2019	Sheet 1 of 4



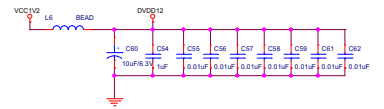
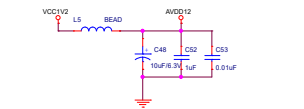
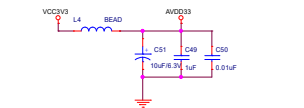
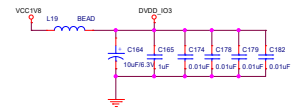
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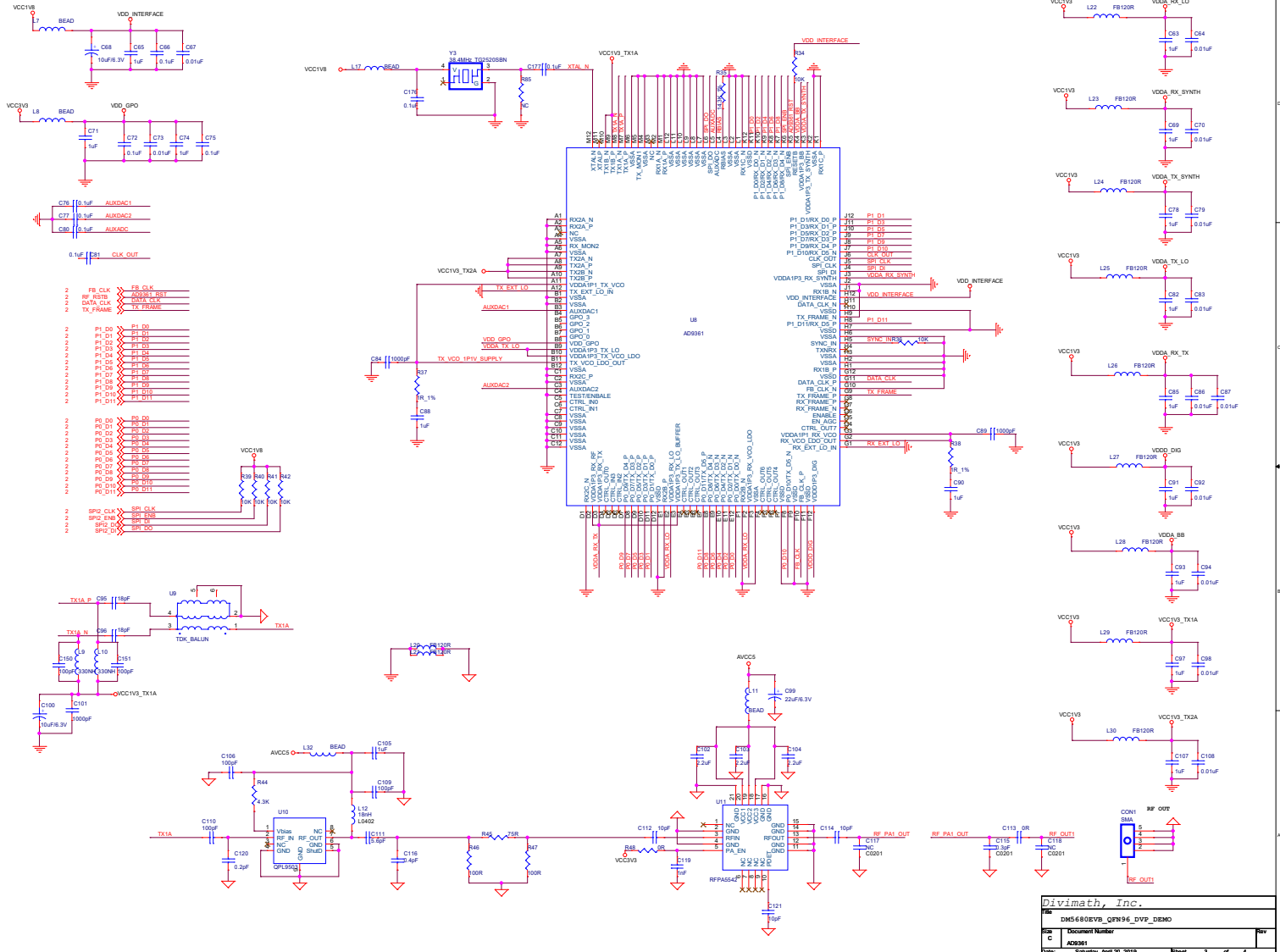


**CTRL\_IO : 3.3V**



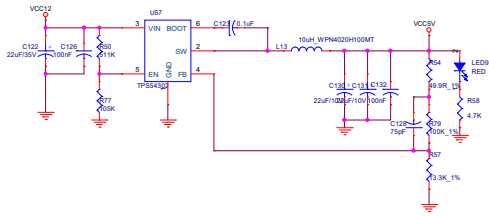
**DATAto9361\_IO : 1.8V**



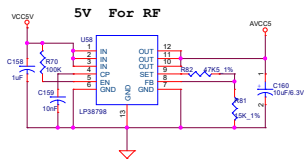


Divimath, Inc.	
File	DMS680EVB_QFN96_DVP_DEMO
Rev	C
Document Number	AD9361
Date	September 20, 2019
Sheet	3 of 4

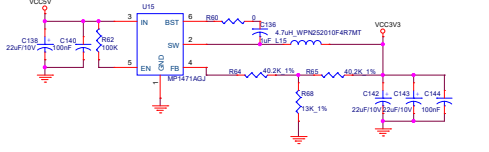
**5.5V 3A**



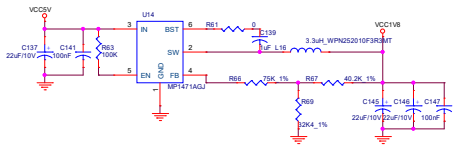
**5V For RF**



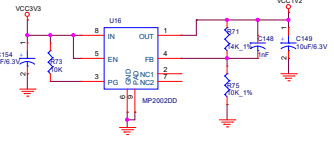
**3.3V**



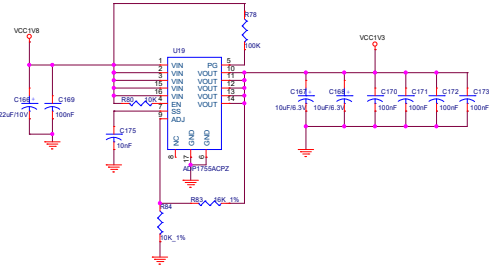
**1.8V**



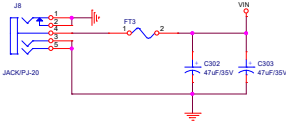
**1.2V**



**1.3V**



**VIN 8V~28V/1A**



Mounting Holes

